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Sneha C Gandh

In recognition of the publication of the paper entitled
Implementation of high speed 16- point FFT processor for OFDM applications using HDL (Verilog)

Published in Volume 2 Issue 7, July-2017, | Impact Factor: 9.15 by Google Scholar

Co-Authors - Sneha M Hiremath, Ashwini A Kotagi, Asha S Tamburi, Prof. Sagar S Birade



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